

Amendments to the Specification:

Please replace paragraph [0074] with the following rewritten paragraph:

[0074] Subsequently, the power source line control signal ~~S_v~~ for SFC for switching the driving-voltage supplying transistor Q_v to on state is supplied from the power source line control circuit 15 to the gate of the driving-voltage supplying transistor Q_v through the power source line control line F. Thus, the driving-voltage supplying transistor Q_v becomes on state, and then the driving voltage V_{dd} is supplied to the source of the driving transistor Q₁.

Please replace paragraph [0097] with the following rewritten paragraph:

[0097] Subsequently, the power source line control signal ~~S_v~~ for SFC for switching the driving-voltage supplying transistor Q_v to on state is supplied from the power source line control circuit 15 to the gate of the driving-voltage supplying transistor Q_v through the power source line control line F. By doing so, the driving-voltage supplying transistor Q_v is switched to on state, and then the driving voltage V_{dd} is supplied to the source of the driving transistor Q₁. At that time, since the driving voltage V_{dd} is always supplied to the second electrode L_b of the holding capacitor C_o independently, regardless of on/off states of the driving-voltage supplying transistor Q_v, the variation in voltage of the holding capacitor can be prevented when the quantity of charge corresponding to the data current I_{data} is held in the holding capacitor C_o and when the driving current I_{el} is supplied from the driving transistor Q₁ to the organic EL element 21 by switching the driving-voltage supplying transistor Q_v to on state. Therefore, the driving current I_{el} corresponding to the voltage V_o held in the holding capacitor C_o is supplied to the organic EL element.